23 July 2012

TO: WHOM IT MAY CONCERN

RE: Non Volatile Memory and User Access on Alta Interface Products

There is a flash part on the Alta interface cards and eNet products that could, in theory, allow the user to store data on the card through power cycle. This part is:

1. **FPGA Flash Boot RAM Part: 64K-2Mbytes.** This part stores the FPGA boot-up image. The user can access this part through a standard API function call ADT_L1_ProgramBoardFlash().   The customer has the source to the API and can remove this function from the API image build.   This would prevent the user from accessing the memory using standard conventions.

This part could be accessible for data storage by an engineer knowledgeable with the system configuration and memory backplane experience, **but the person would have to be intentionally trying to manipulate memory for non standard purposes**. Normal application development or driver testing would not need to access this memory.

Alta provides a standard hardware option (a resistor network configuration) that can inhibit the write capability of this part. This inhibit option ("-N" part number suffix) must be designated at order time, or the cards may be sent back to the factory for upgrade/rework for a fee. This option would also prevent in-field updates, which means, product would need to be sent back to the factory for updates.

If there are any questions, please contact the undersigned.

Best Regards,

Richard Schuh
CEO
Alta Data Technologies
805-964-5390
rick.schuh@altadt.com
www.altadt.com